

1c714 U.S. PTO  
09/585427  
06/03/00

257	59	Subclass
Class	ISSUE CLASSIFICATION	

PATENT NUMBER

6380559



6380559

U.S. UTILITY Patent Application

SCANNED	OLP.E.	PATENT DATE
	G.A.	APR 30 2002

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/585427	F	257	59	2811 2826	TRAN, M.

APPLICANTS  
Park Woon-Yang  
Yoon Jong-Soo  
Jeong Chang-Oh

TITLE  
Thin film transistor array substrate for a liquid crystal display and a method for fabricating the same

PTO 2040  
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	59	257	72						
INTERNATIONAL CLASSIFICATION		349	106	143	158				
H01L	29/04								
G02F	1/136								
G02F	1/1335								

4/1/02 Formal Drawings 74/75/76 6/2/00

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg. 74 Figs. Drwg. 76 Print Fig. 45			<b>CLAIMS ALLOWED</b> Total Claims 17 Print Claim for O.G. 1	
	(Assistant Examiner) _____ (Date) _____ Minh Lee Tran Primary Examiner (Primary Examiner) _____ (Date) 12/01			<b>NOTICE OF ALLOWANCE MAILED</b> 1/2/02 <b>ISSUE FEE</b> 1280.00 Amount Due 1280.00 Date Paid 215.02	
	(Legal Counsults Examiner) _____ (Date) 1/3/02			<b>ISSUE BATCH NUMBER</b>	
	<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code, Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

Form PTO-438A  
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)

(FACE)